

ABSTRACT OF THE DISCLOSURE

A semiconductor device operated in modes that may be re-set. The semiconductor device includes mode setting fuse
5 circuits, each having a plurality of fuses. Each mode setting fuse circuit stores a code for setting the mode in accordance with the breakage pattern of the fuses. A fuse information selection circuit is provided with the codes from the mode setting fuse circuits. The fuse information
10 selection circuit provides a code selection circuit with the code having the highest priority. The code selection circuit provides a decoding circuit with the high priority code. When a determination circuit receives an invalidating signal from an invalidation fuse circuit, the code selection
15 circuit provides the decoding circuit with a code of a program circuit.